THE COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: RICHARD BULLOCK AND DAVID PAUL JONES

For: METHOD OF FABRICATING A GATE DIELECTRIC LAYER FOR A THIN FILM TRANSISTOR





losed are:

3 sheets of drawing(s) - formal.

An assignment of the invention to ESM Limited

An associate power of attorney | Applicant claims small entity status

Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

Section 1	(Col. 1)	(Col. 2)	OTHER THAN A	SMALL ENTITY
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE				\$ 710.
TOTAL CLAIMS	15 -20=	0	x 18 =	\$ 0.
INDEP CLAIMS	2 -3=	0	x 80 =	\$ 0.
MULTIPLE DEPENDENT CLAIM PRESENTED + 260 =				
STATE OF THE STATE		sı	JB TOTAL	\$ 710.
The state of the s		A	SSIGNMENT	\$40.
		T	OTAL	\$ 750.

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X Any additional filing fees required under 37 CFR §1.16.

X Any patent application processing fees under 37 CFR §1.17.

Respectfully submitted, STEPHEN R ACKERMAN, REG. NO. 37,761